



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q77369

USUI, Minoru, et al.

Appln. No.: 10/662,397

Group Art Unit: 2856

Confirmation No.: 1476

Examiner: Andre JACKSON

Filed: September 16, 2003

For: LIQUID CONSUMPTION STATUS DETECTING METHOD, LIQUID CONTAINER,
AND INK CARTRIDGE

SUBMISSION OF EXECUTED DECLARATION UNDER 37 C.F.R. §1.132

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith is a copy of an executed Declaration Under 37 C.F.R. §1.132 signed
by Minoru USUI, Kenji TSUKADA, Munehide KANAYA, Noboru TAMURA.

Respectfully submitted,

Jeffrey A. Schmidt
Registration No. 41,574

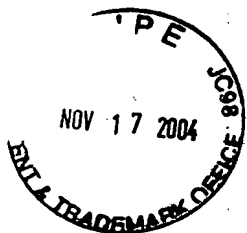
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WASHINGTON OFFICE

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CUSTOMER NUMBER

Date: November 17, 2004



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Docket No: Q77369

Minoru USUI, et al.

Appln. No.: 10/662,397

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Confirmation No.: 1476

Examiner: Andre Jackson

Filed: September 16, 2003

For: **LIQUID CONSUMPTION STATUS DETECTING METHOD, LIQUID CONTAINER,
AND INK CARTRIDGE**

DECLARATION UNDER 37 C.F.R. § 1.132

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

We, Minoru USUI, Kenji TSUKADA, Munehide KANAYA, and Noboru TAMURA, hereby declare and state:

THAT we have read and understand the specification of the present patent application, as identified above, including the claims;

THAT we have read and understand the specification of US Patent Application Publication 2003-0140694 (hereinafter Usui '694), including its claims;

THAT in the present application, the subject matter of claims 20-23, 28-30, 33, and 34, has been invented by Minoru USUI, Kenji TSUKADA, and Munehide KANAYA;

THAT in the present application, the subject matter in claims 24-27, 31, and 32, has been invented by Minoru USUI, Kenji TSUKADA, Munehide KANAYA, and Noboru TAMURA;

THAT, specifically, Noboru TAMURA, together with Minoru USUI, Kenji TSUKADA, Munehide KANAYA contributed to the specific arrangement of the circuit segment such as type of transistor or FET, amplifiers, or other signals as recited in claims 24-27, 31, and 32, whereas

Declaration Under 37 C.F.R. § 1.132
US Appln. 10/662,397

Atty. Docket: Q77369

Minoru USUI, Kenji TSUKADA, and Munehide KANAYA contributed to the general or broad concept of a circuit segment.

We declare further that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: Oct, 6, 2004

Minoru Usui
Minoru USUI

Date: Oct. 1, 2004

Kenji Tsukada
Kenji TSUKADA

Date: Oct 13, 2004

Munehide Kanaya
Munehide KANAYA

Date: October 4, 2004

Noboru Tamura
Noboru TAMURA